

Electrostatic Discharged Protection Devices (ESD) Data Sheet

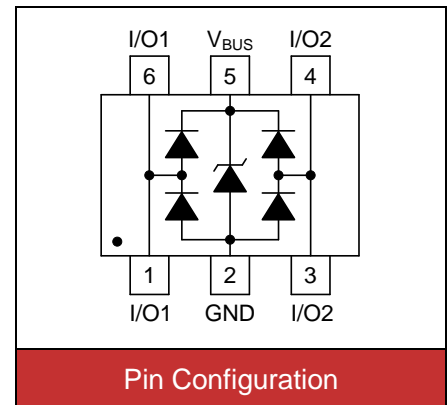
Description

UBT26A05L03 is designed to protect high speed data interfaces. It has been specifically designed to protect sensitive components which is connected to data and transmission lines from overvoltage caused by electrostatic discharge (ESD), electrical fast transients (EFT), and lightning.



Features

- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOT23-6L surface mount package
- Protects three data lines
- Working voltage: 5V
- Low leakage current
- Low clamping voltage
- Solid-state silicon avalanche technology
- Lead Free/RoHS compliant
- Solder reflow temperature: Pure Tin-Sn, 260~270°C
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020
- AEC-Q101 qualified
- Marking: LC62



Maximum Ratings

Rating	Symbol	Value	Unit
Peak pulse current (tp=8/20µs)	I_{PP}	5	A
ESD voltage (Contact discharge)	V_{ESD}	±8	kV
ESD voltage (Air discharge)		±15	
Storage & operating temperature range	T_{STG}, T_J	-55~+150	°C

Electrical Characteristics ($T_J=25^{\circ}\text{C}$)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V_{RWM}				5.25	V
Reverse breakdown voltage	V_{BR}	$I_{BR}=1\text{mA}$	6			V
Reverse leakage current	I_R	$V_R=5.25\text{V}$ Each I/O pin			1	μA
Clamping voltage ($t_p=8/20\mu\text{s}$)	V_C	$I_{PP}=1\text{A}$			12	V
Clamping voltage ($t_p=8/20\mu\text{s}$)	V_C	$I_{PP}=5\text{A}$			17	V
Off state junction capacitance	C_J	0Vdc, f=1MHz Between I/O pins and GND		3.5		pF

Typical Characteristics Curves

Figure 1. Power Derating Curve

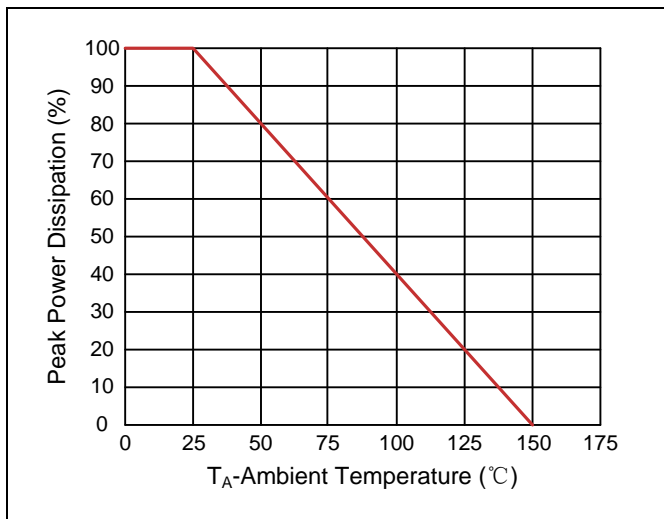


Figure 2. Pulse Waveforms

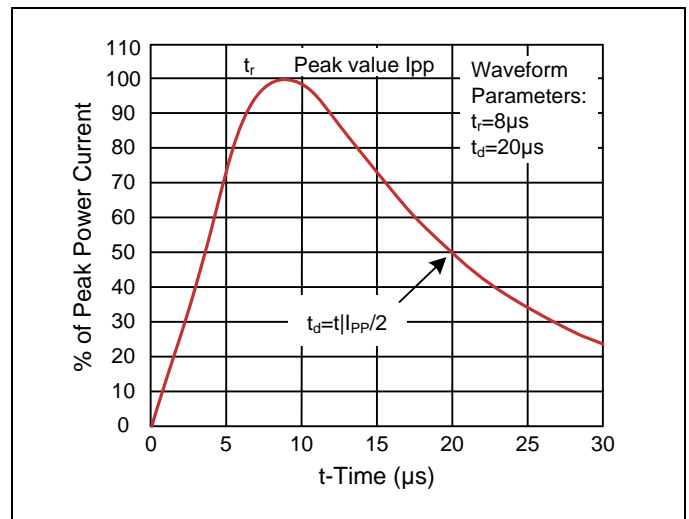


Figure 3. Clamping Voltage vs. Peak Pulse Current

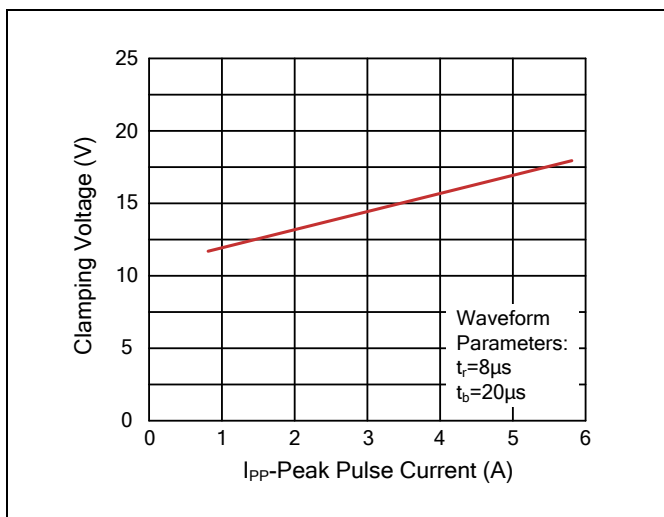
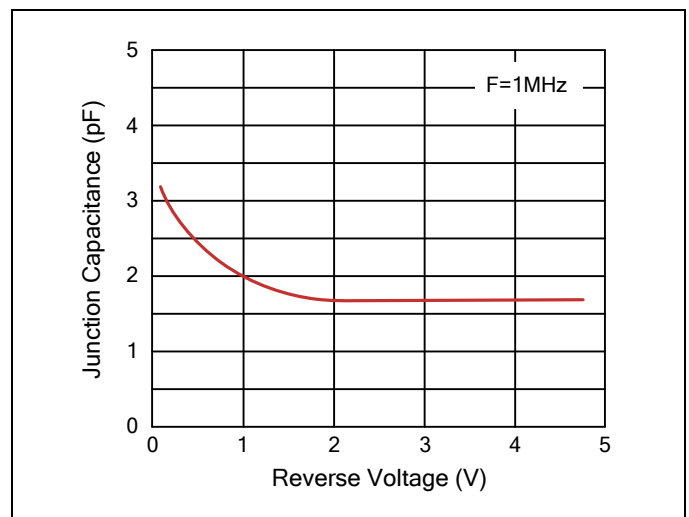
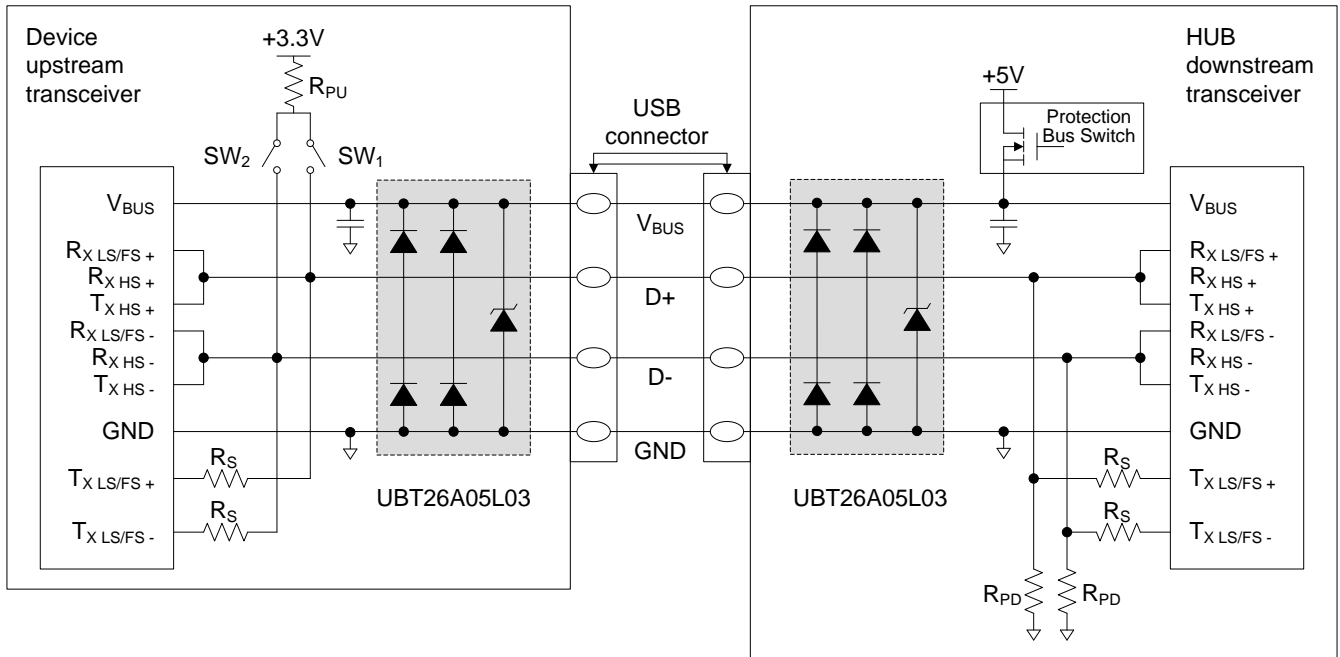


Figure 4. Normalized Capacitance vs. Reverse Voltage



Applications Information

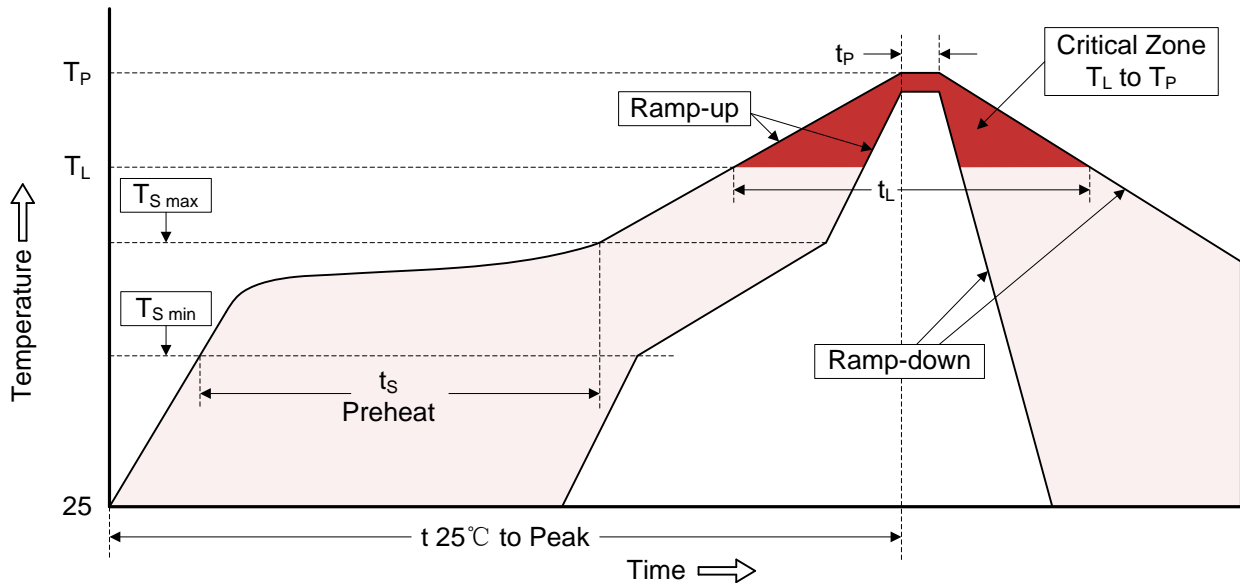
USB 2.0 port application diagram



Mode	SW ₁	SW ₂
Low Speed LS	Open	Closed
Full Speed FS	Closed	Open
High Speed HS	Closed then open	Open

Recommended Soldering Conditions

Reflow Soldering



Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	3°C/second max.
Preheat -Temperature Min ($T_{S\ min}$) -Temperature Max ($T_{S\ max}$) -Time (min to max) (t_s)	150°C 200°C 60-180 seconds
$T_{S\ max}$ to T_L -Ramp-up Rate	3°C/second max.
Time maintained above: -Temperature (T_L) -Time (t_L)	217°C 60-150 seconds
Peak Temperature (T_P)	260°C
Time within 5°C of actual Peak Temperature (t_P)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

Dimensions (SOT23-6L)

Symbol	Dimension			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	2.80	3.00	0.110	0.118
B	2.60	3.00	0.102	0.118
C	0.93	0.97	0.037	0.038
D	0.41		0.016	
E	1.50	1.70	0.059	0.067
F	0.11	0.19	0.004	0.007
G	-	0.10	-	0.004
H	0.40	-	0.016	-
J	1.00	1.20	0.393	0.047

Recommended Soldering Pad Layout

0.90 (6X)
0.60 (6X)
0.95
2.30

Packaging

Tape	Symbol	Dimension (mm)
	W	8.00±0.30
P0	4.00±0.10	
P1	4.00±0.10	
P2	2.00±0.10	
D0	Φ1.55±0.10	
D1	Φ1.05±0.05	
E	1.75±0.10	
F	3.50±0.10	
A	3.40±0.10	
B	3.20±0.10	
K	1.30±0.10	
t	0.25±0.05	
Reel	D	Φ178.0±2.0
	D2	Φ13.0
	W1	9.5
		Quantity: 3000PCS