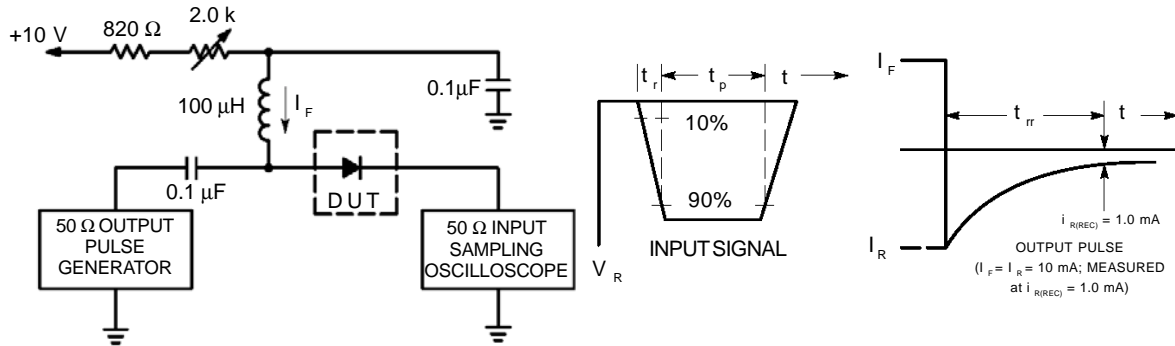




LMDL6050T1G,S-LMDL6050T1G



- Notes: 1. A 2.0 kΩ variable resistor adjusted for a Forward Current ( $I_F$ ) of 10mA.
- 2. Input pulse is adjusted so  $I_{R(peak)}$  is equal to 10mA.
- 3.  $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

TYPICAL CHARACTERISTICS

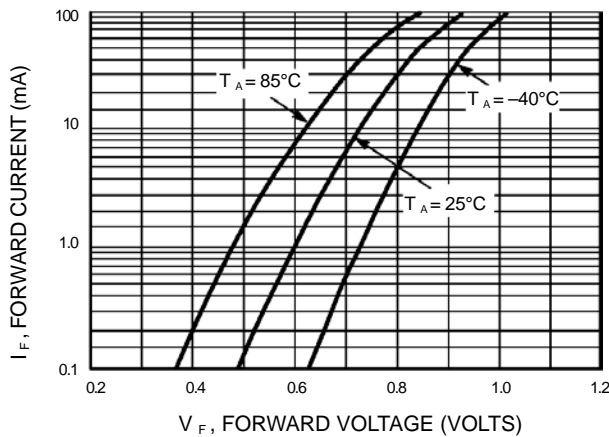


Figure 2. Forward Voltage

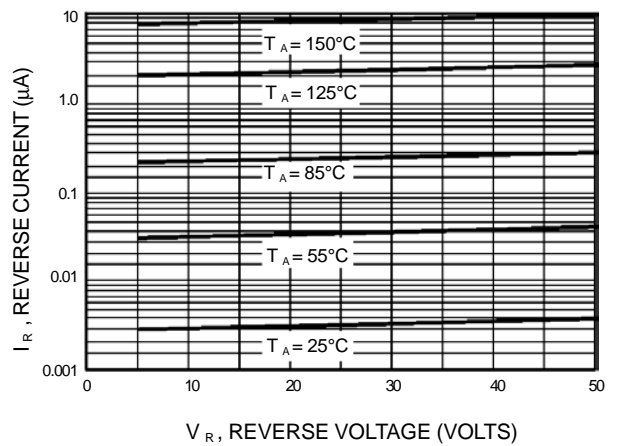


Figure 3. Leakage Current

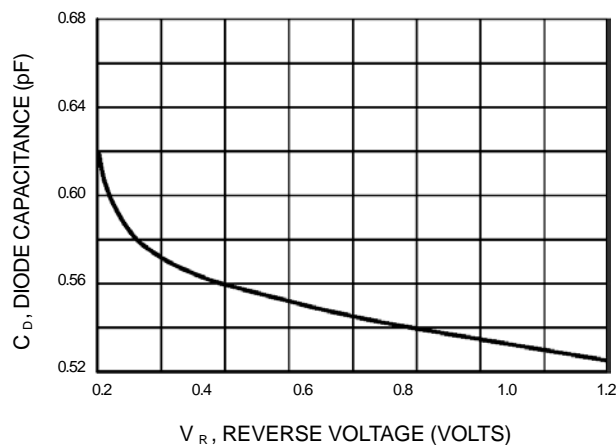
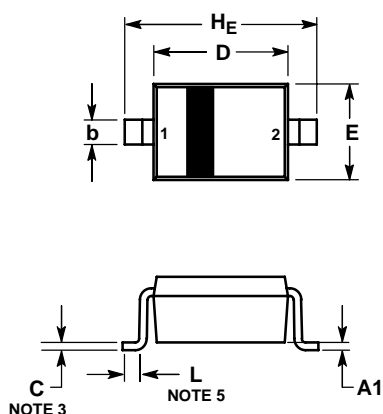


Figure 4. Capacitance

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- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH SOLDER PLATING.
  4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
  5. DIMENSION L IS MEASURED FROM END OF RADIUS.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	0.90	1.00	0.031	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.15 REF			0.006 REF		
b	0.25	0.32	0.4	0.010	0.012	0.016
C	0.089	0.12	0.177	0.003	0.005	0.007
D	1.60	1.70	1.80	0.062	0.066	0.070
E	1.15	1.25	1.35	0.045	0.049	0.053
L	0.08			0.003		
HE	2.30	2.50	2.70	0.090	0.098	0.105

SOLDERING FOOTPRINT\*

